

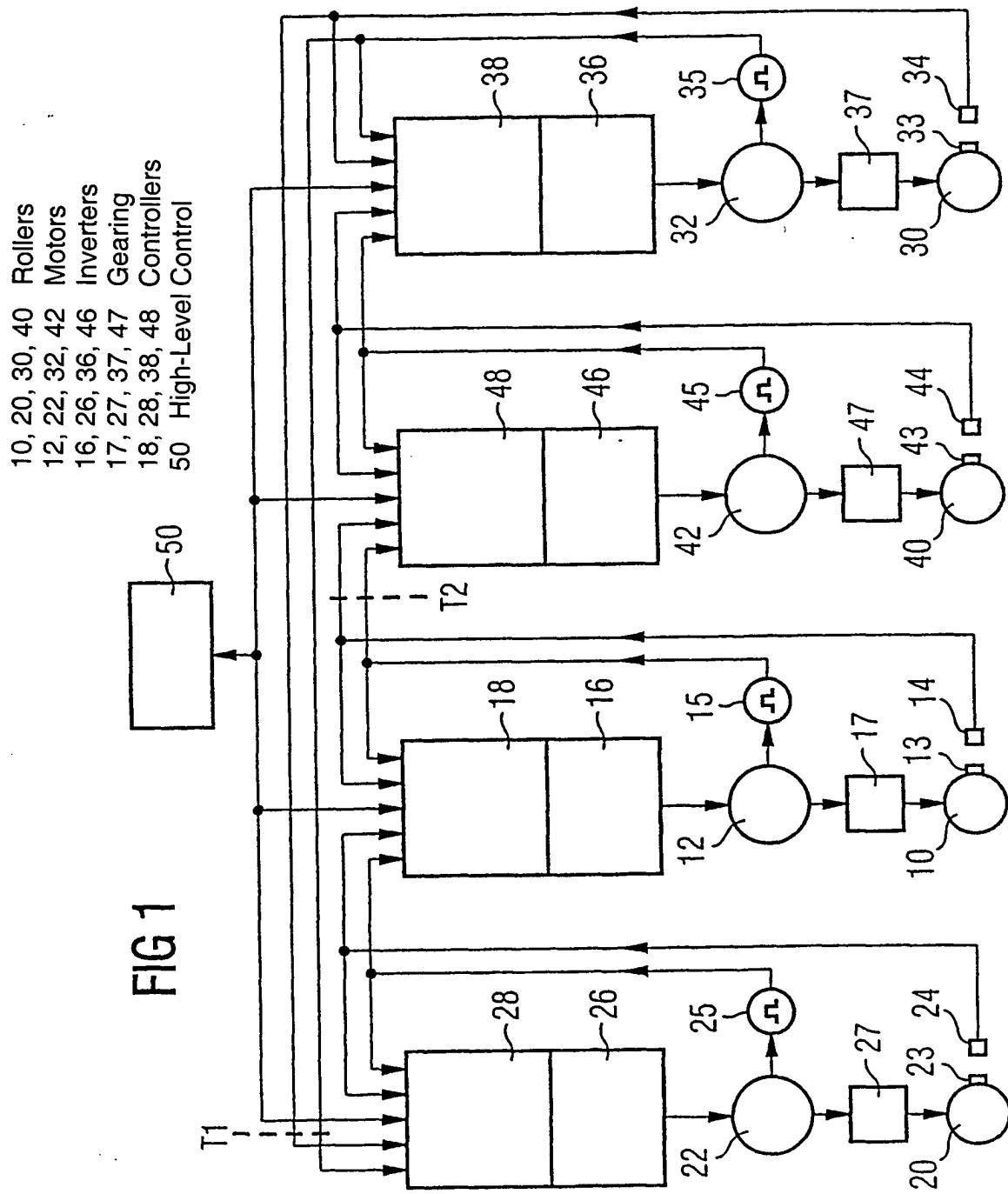
Decoding

50

18, 28, 38, 48

Controllers

High-Level Control



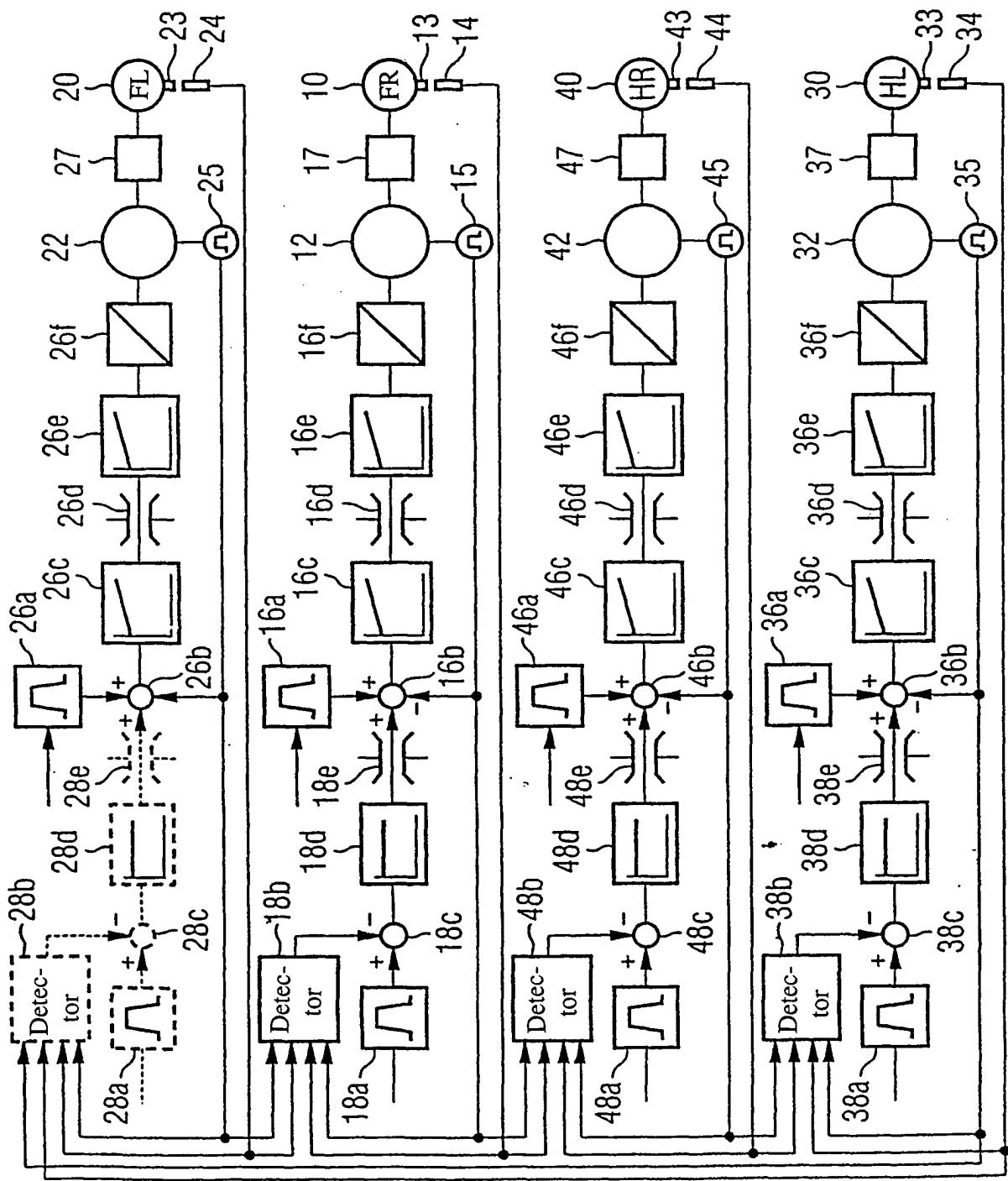


FIG 2

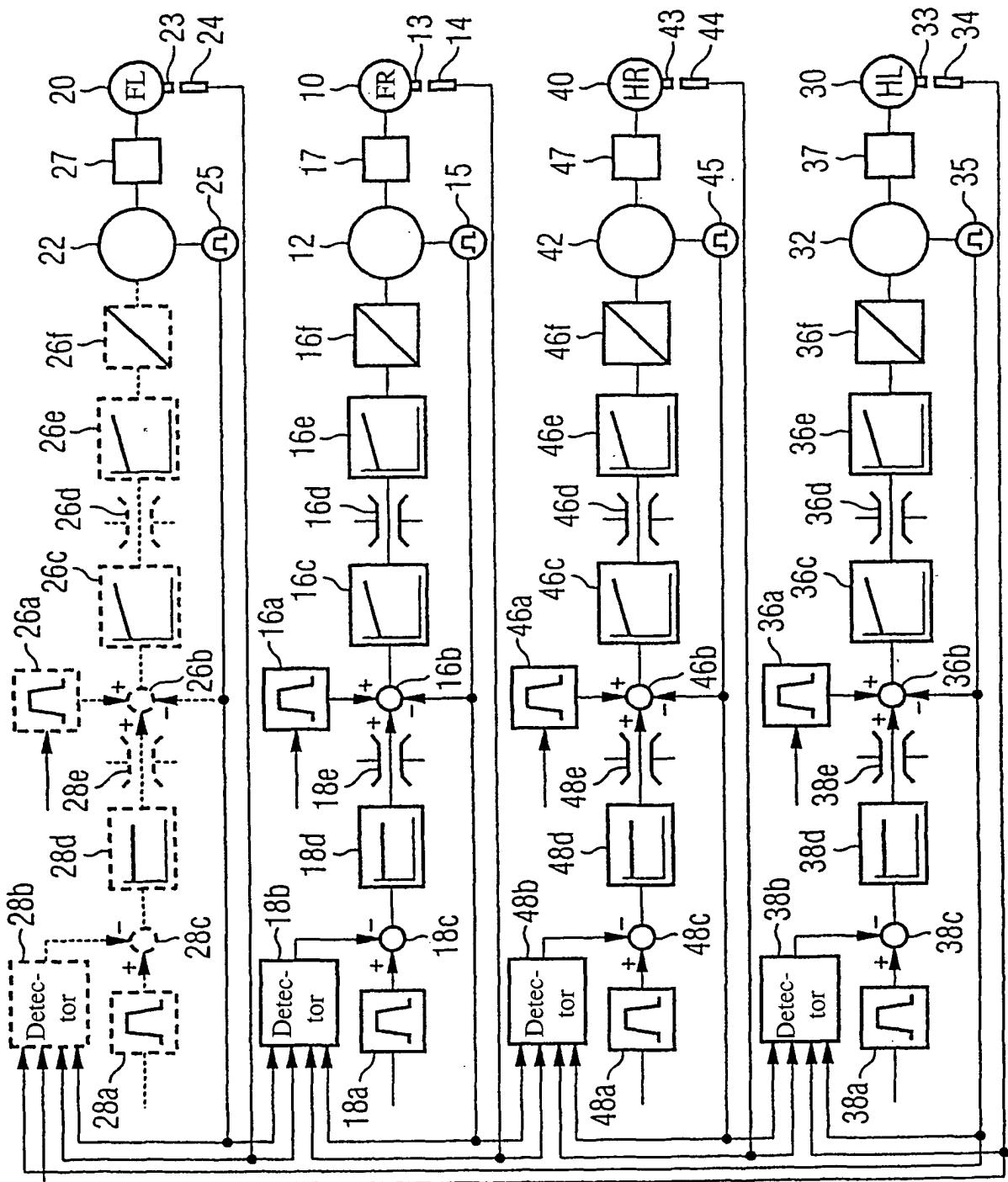


FIG 3